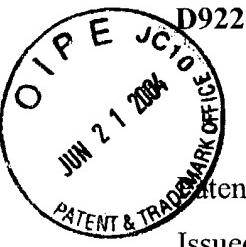


*Cafc*  
PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2814

Patent Number: 6,727,143 B1

Issued: April 27, 2004

Name of Patentee: Hui et al.

Assignee: Advanced Micro Devices, Inc., Fujitsu Limited,  
Fujitsu and Semiconductor Ltd.

Title of the Invention: METHOD AND SYSTEM FOR  
REDUCING CHARGE GAIN AND  
CHARGE LOSS WHEN USING AN  
ARC LAYER IN INTERLAYER  
DIELECTRIC FORMATION

**CERTIFICATION UNDER**

**37 C.F.R. § 1.8**

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*Serena Beller*

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**FOR PTO'S MISTAKE (37 C.F.R. § 1.322(a))**

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3. Since the Certificate of Correction is necessary to correct mistakes made by the PTO in printing this patent, Patentees are not required to submit a fee herewith.

Respectfully submitted,

WINSTEAD SECHREST & MINICK P.C.

Attorney for Assignee

By:

*RAVJ*  
Robert A. Voigt, Jr.  
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P R I N T E R S	<p><b>UNITED STATES PATENT AND TRADEMARK OFFICE</b></p> <p><b>CERTIFICATE OF CORRECTION</b></p> <p>PATENT NO. : 6,727,143 B1 DATED : April 27, 2004 INVENTOR(S) : Angela T. Hui; Mark T. Ramsbey; Yu Sun; David H. Matsumoto</p> <p>It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:</p> <p>Column 1, Line 28, following "which" please delete "is". Column 1, Line 51, following "ARC" please delete "so". Column 3, Line 4, following "further" please delete "so". Column 6, Line 29, following "to the" please delete "in".</p>
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